

What is claimed is:

1. A high-frequency circuit device comprising:

5 a distribution circuit for distributing a signal inputted from a signal input terminal to a plurality of first lines through a branch portion;

10 a synthetic circuit for combining signals inputted from a plurality of second lines into one through a combined portion as an output signal and outputting the same from a signal output terminal;

15 transistors respectively placed between one ends of each individual first lines of said distribution circuit and one ends of each individual second lines of said synthetic circuit; and

isolators respectively connected between said transistors and the signal input terminal and between said transistors and the signal output terminal.

2. The high-frequency circuit device according to claim 1,

20 wherein the first and second lines respectively have impedance converter circuits and said isolators are respectively connected between said transistors and the impedance converter circuits.

3. The high-frequency circuit device according to claim 1,

25 wherein said transistors and first lines, and said transistors and second lines are respectively connected through the isolators, the impedance of said each isolator on said transistor side is matched to that of said each transistor, and the values of impedance of said isolators on the first and second line sides are set higher than the impedance on said transistor side.

4. The high-frequency circuit device according to claim 1,

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wherein said isolators are provided at the branch portion of said distribution circuit and the combined portion of said synthetic circuit, said isolators placed at the branch portion are respectively connected to the first lines different from one another with both line ends of their output ports as signal line ends, and said isolators placed at the combined portion are respectively connected to the second lines different from one another with both line ends of their input ports as signal line ends.

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5. A high-frequency circuit device comprising:

a distribution circuit for distributing a signal inputted from a signal input terminal to a plurality of first lines respectively having a plurality of first impedance converter circuits through a branch portion;

a synthetic circuit for combining signals inputted from a plurality of second lines each having a second impedance converter circuit into one through a combined portion as an output signal and outputting the same from a signal output terminal;

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transistors respectively placed between one ends of each individual first lines of said distribution circuit and one ends of each individual second lines of said synthetic circuit; and

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isolators connected either between said transistors and the branch portion of said distribution circuit or between said

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transistors and the combined portion of said synthetic circuit.

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